a) An abstract model of the structure of a flash chip

b) Wear leveling

a) Two pages A and B written into a fresh block B.

b) Two new pages, C and D, and two pages replacement, A' and B' written into block B. Original pages, A and B, are now stale (in hatches).

c) As pages A and B cannot be rewritten until the whole block B is erased, all good pages, A', B', C and D are reread and written to a new block Y. The old block B is then erased.

c) Garbage Collection